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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	10/034,528
		First Named Inventor	Eldridge et al.
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		Examiner Name	Paresh H. Patel
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	284	WO	1992/020842		Cornell Research Foundation, Inc.	16-Nov-92	Yes	
	285	WO	1993/018382		Kopin Corporation	16-Sep-93	Yes	
	286	WO	1994/012887		Micromodule Systems, Inc.	9-Jun-94	Yes	
	287	WO	1994/029671		DANISCH, Lee, A..	22-Dec-94	Yes	
	288	WO	1997/009584		IBM Corporation	13-Mar-97	Yes	
	289	WO	1998/026300		GGB Industries, Inc.	18-Jun-98	Yes	

OTHER PRIOR ART – NONPATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Copy Enclosed	T ²
	290	"Probe Card Stiffener With Theta Adjust," IBM Technical Bulletin (2 pages) (no publication information)	Yes	
	291	"Ultra-Small Test Probe" (2 pages) (No publication information)	Yes	
	292	BARSOTTI C., et al. "Very High Density Probing," (Tektronix), 1988 International Test Conference, pp.608-614, (1988).	Yes	
	293	BEILEY Mark et al., "A Micromachined Array Probe Card-Characterization," IEEE Transactions on Components, Packaging, and manufacturing Tehcnology - Part B, Vol. 18, No.1, pp.184-191, February 1995.	Yes	
	294	BURNS F.C. et al., "Fixed Probe Test Adapter," Research Disclosure, No. 319, 90A064135, November 1990.	Yes	
	295	DA-YUAN Shih, "Elasticon-Integrated Probe Technology," (IBM), June 1995.	Yes	
	296	DIETRICH T.R. et al. "Photoetchable Glass for Microsystems: tips for atomic force microscopy", J. of Micromechanics and Microengineering, ISSN: 0960-1317, Vol. 3, No. 4, pp.187-189, December 1993.	Yes	
	297	GENIN D. et al., "Probing Considerations in C-4 Testing of IC Wafers," IBM, The International J. of Microcircuits and Electronic Packaging, (ISSN 1063-1674), Vol. 15, No. 4, pp 229-238, Fourth Quarter 1992.	Yes	

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	298	Hirano T. et al, "Silicon Microprobe Array for Testing and Burn-In," (IEEE), pp.89-94, July 1994.	Yes	
	299	KIEWITT D.A., "Microtool fabrication by etch pit replication." Rev. Sci. Instum., Vol.44, No.12, pp.1741-1742, 1973.	Yes	
	300	LAIL P. et al., "Chapter 5 - Interconnections and Connectors", Handbook of Electronic Package Design, pp.239-255, 1991.	Yes	
	301	LESLIE B. et al. "Membrane Probe Card Technology," IEEE, (1988 International Test Conference), pp.601-607, 1988.	Yes	
	302	MACHIDA K., "Interconnect Technology of the 1990s", Surface Mount Technology, pp. 3-12.	Yes	
	303	MILOSEVIC I. et al., "Straight Wall Bumps for High Lead Count Devices: Photolithography and Physical Properties", Materials Research Society Symposium Proceedings, Vol. 154, pp.425-440, 1989.	Yes	
	304	OETINGER S. et al, "Needless Probe Card," Motorola Technical Developments Bulletin, Vol. 8, pp.45-46, October 1988.	Yes	
	305	STIX Gary, "Micron Machinations", published in Scientific American, pp.72-80, November 1992.	Yes	
	306	WASSINK R.J. Klein, "Soldering In Electronics, Chapter 12.2 – Good Soldered Joints", 2nd ed., pp.647-650, 1989.	Yes	
	307	YOUNKIN L., "Thin-Film-Hybrid Wafer-Probe Card Promises Speed and Density," Electronics Test, pp. 26-30, May 1989.	Yes	
	308	"Phicom Corporation's Amended Invalidity And Unenforceability Contentions," FormFactor, Inc. v. Phicom Corporation, United States District Court District of Oregon (Case No. 05-6062-HO)	Yes	
	309	"Defendants' Patent Local Rule 3-3 Preliminary Invalidity Contentions For U.S. Patent Nos. 6,246,247, 6,509,751, 6,624,648, And 7,073,254," FormFactor, Inc. v. Micronics Japan Co. and MJC Electronics Corp., United States District Court Northern District of California (Case No. 3:06-CV-07159 JSW)	Yes	

Examiner Signature	Date Considered
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